

Figure 1

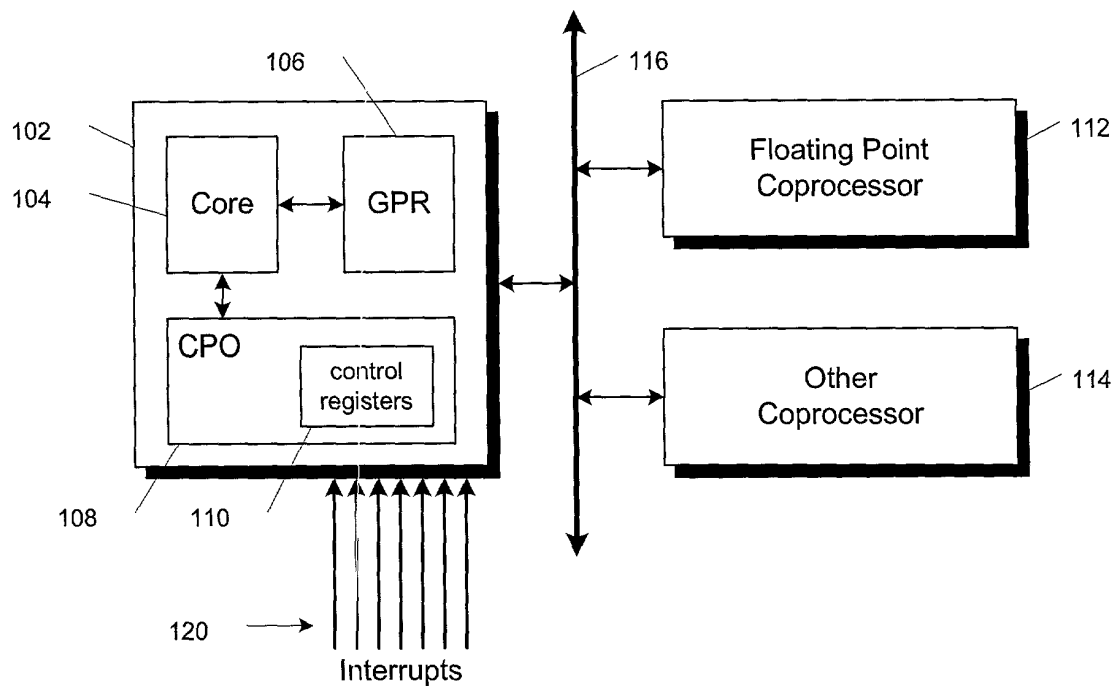


Figure 2

STATUS REGISTER FORMAT

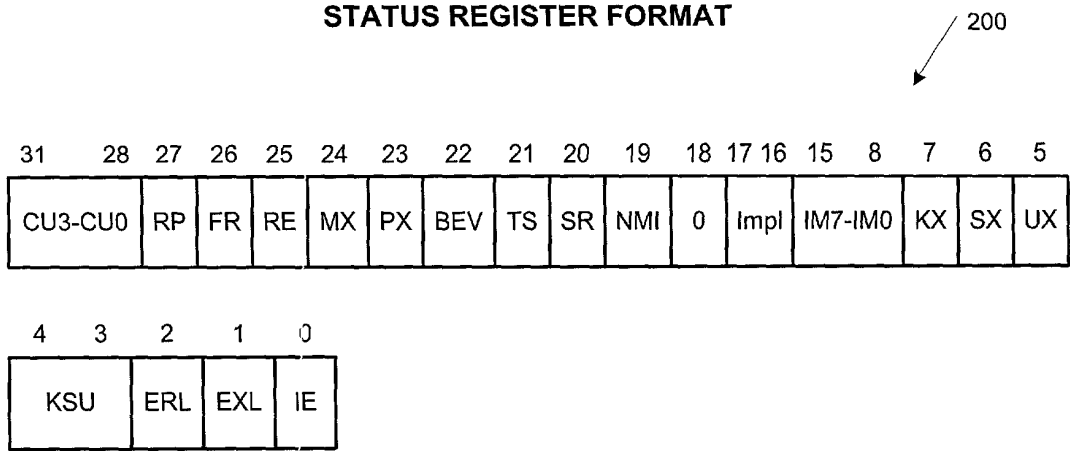


Figure 3

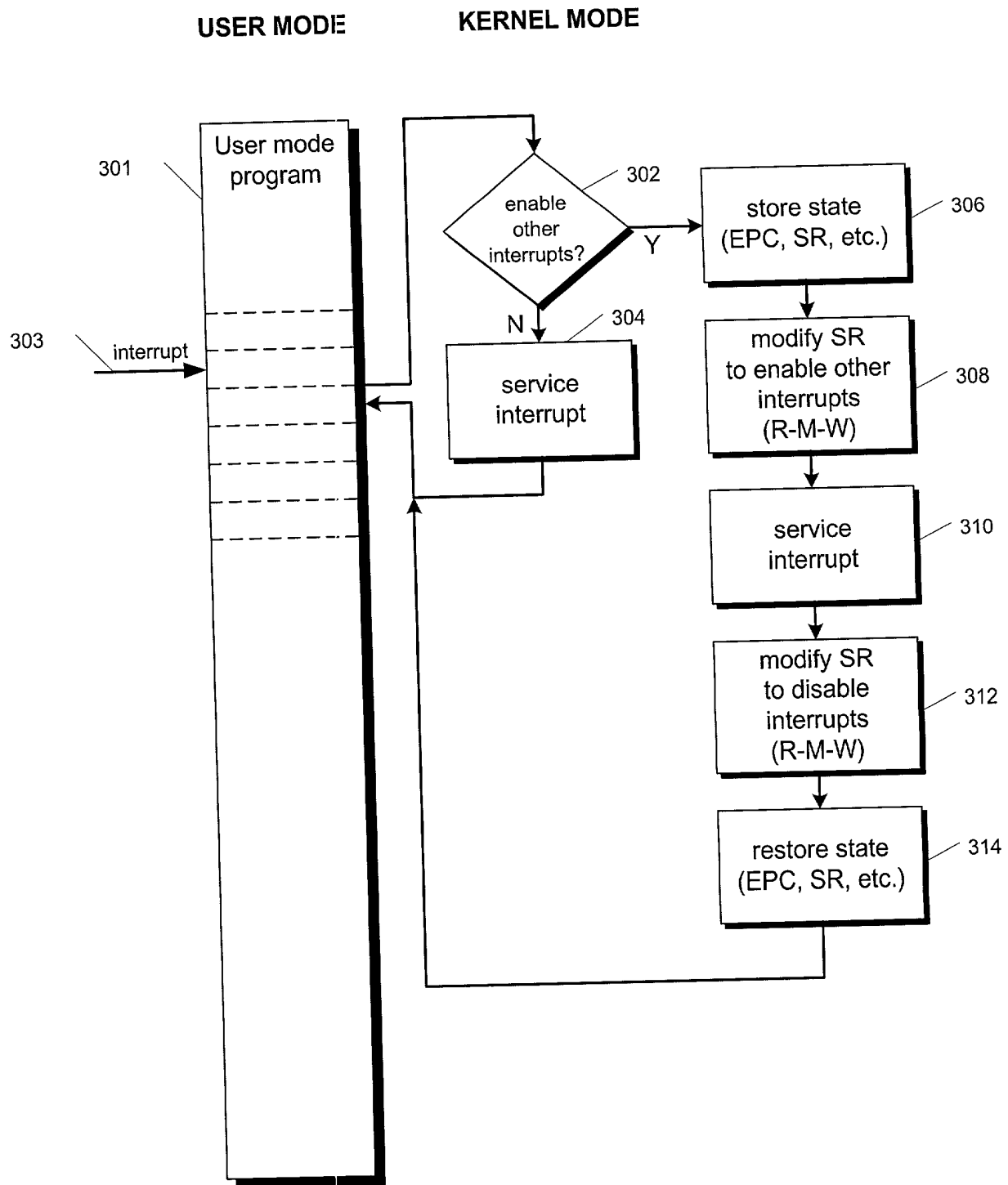


Figure 4

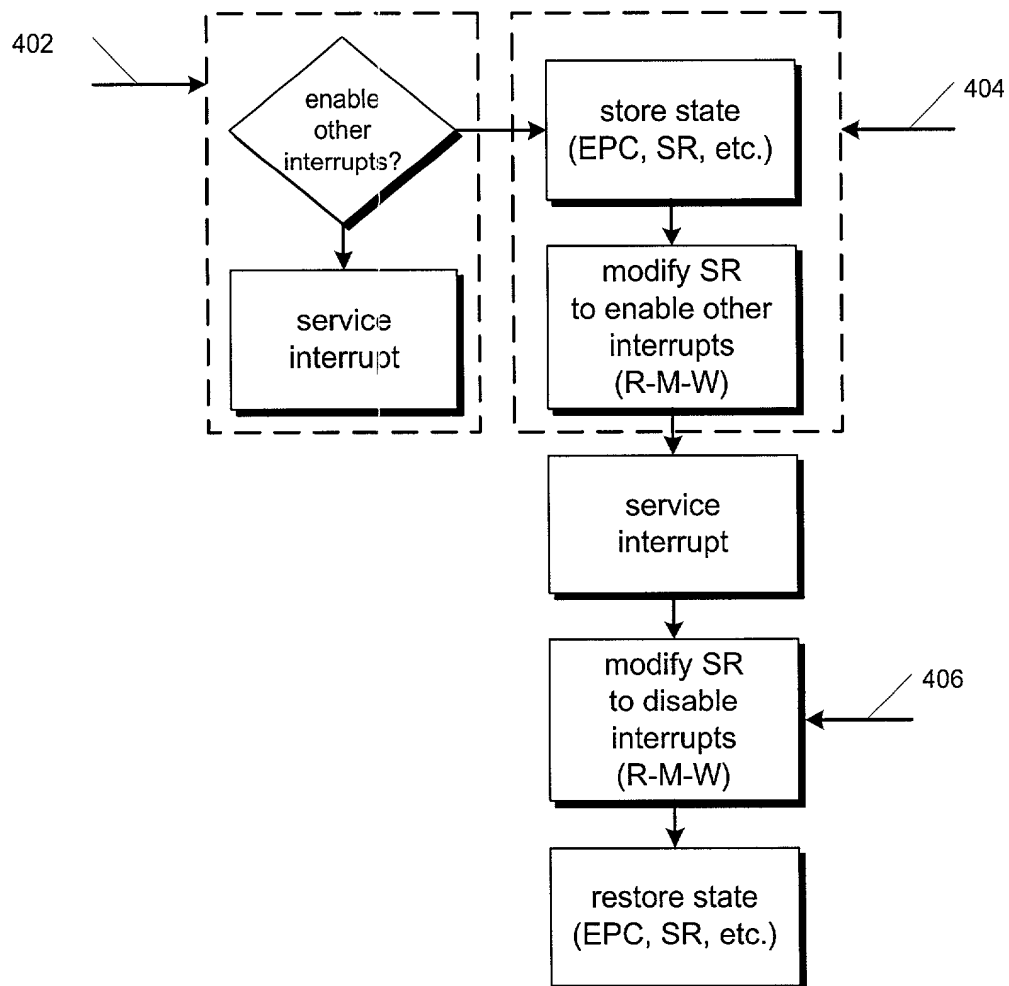


Figure 5

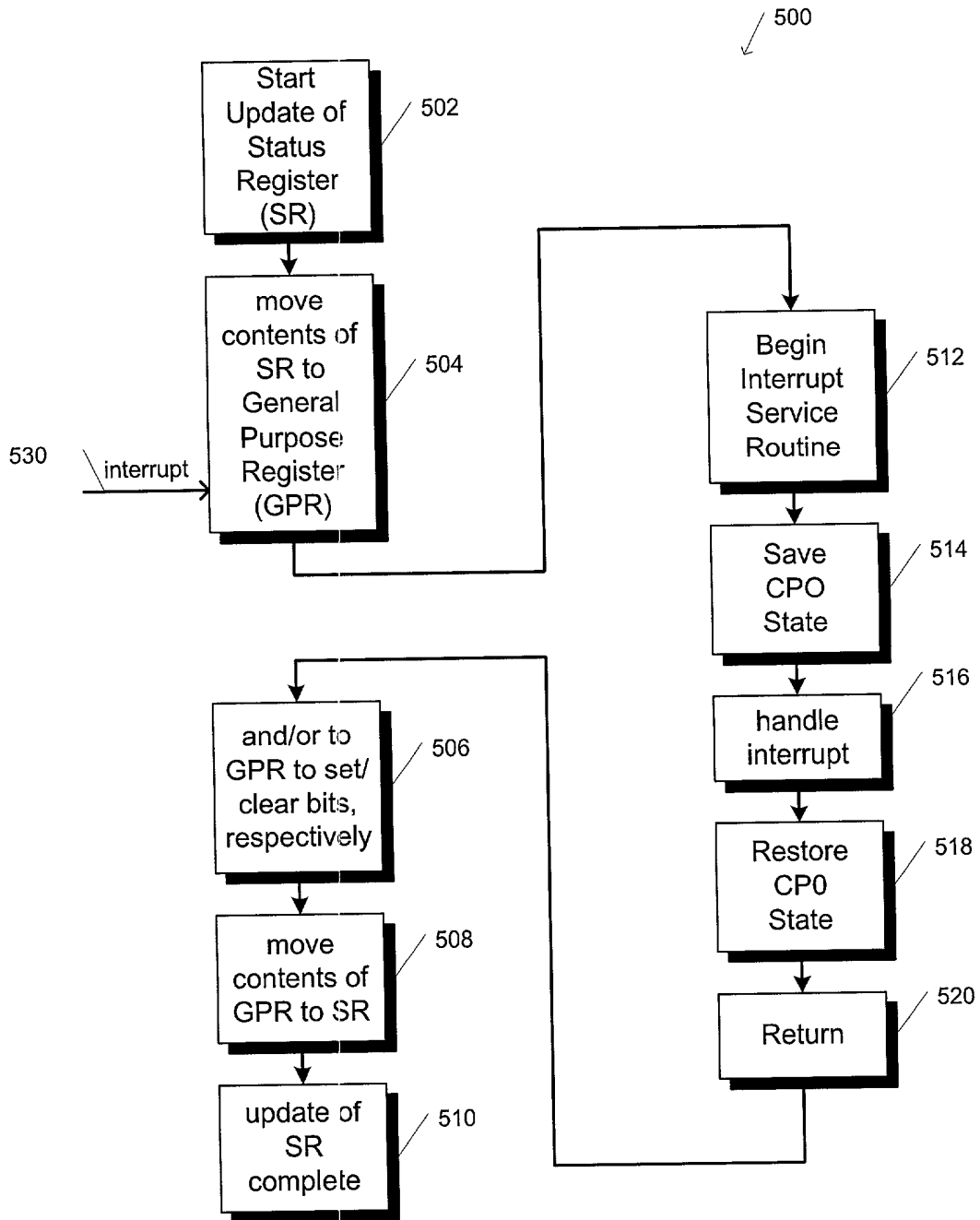


Figure 6

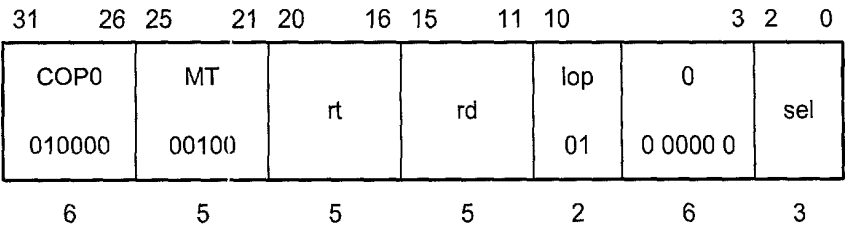
600
↙

Mnemonic	Function
BICC0	Bit Clear Coprocessor Zero
BISC0	Bit Set Coprocessor Zero
DBICC0	Doubleword Bit Clear Coprocessor Zero
DBISC0	Doubleword Bit Set Coprocessor Zero

Figure 7

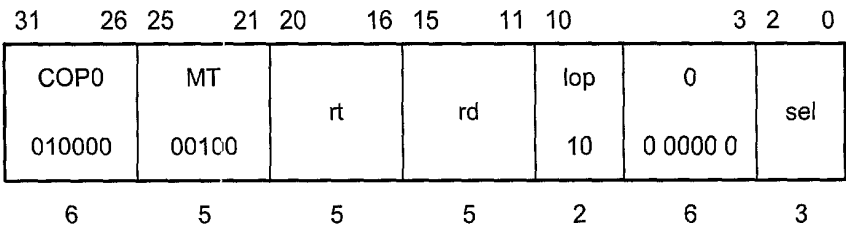
Bit Clear to Coprocessor 0

700



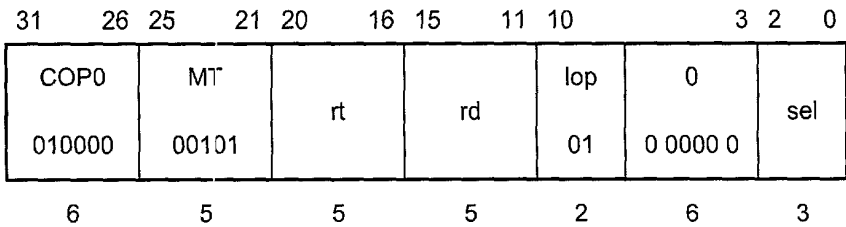
Bit Set to Coprocessor 0

702



Double Bit Clear to Coprocessor 0

704



Double Bit Set to Coprocessor 0

706

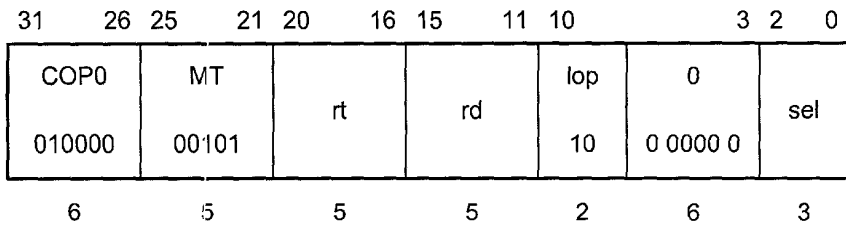


Figure 8

